

EAST Search History**EAST Search History (Prior Art)**

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	53080	"370"/\$.ccls. and (@ad<"20020812" or @rlad<"20020812")	USPAT	OR	OFF	2011/07/07 13:45
L2	3	L1 and (induct\$4 same impedance same matc\$4 same switch)	USPAT	OR	OFF	2011/07/07 13:47
L3	5	L1 and (induct\$4 same impedance same matc\$4) and (ethernet same switch)	USPAT	OR	OFF	2011/07/07 13:50
L4	18	(induct\$4 same impedance same matc\$4) and (ethernet same switch)	USPAT	OR	OFF	2011/07/07 13:55
L5	34	(induct\$4 same impedance same matc\$4) and (ethernet same switch)	US-PGPUB; USPAT; USOCR; DERWENT; IBM_TDB	OR	OFF	2011/07/07 13:59
L6	19802	(induct\$4 same impedance same matc\$4)	US-PGPUB; USPAT; USOCR; DERWENT; IBM_TDB	OR	OFF	2011/07/07 14:04
L7	7325	(induct\$4 same impedance same matc\$4) and switch	US-PGPUB; USPAT; USOCR; DERWENT; IBM_TDB	OR	OFF	2011/07/07 14:05
L8	1118	(induct\$4 same impedance same matc\$4) and ((offset or compensat\$3 or cance\$4) same switch)	US-PGPUB; USPAT; USOCR; DERWENT; IBM_TDB	OR	OFF	2011/07/07 14:06
L9	686	(induct\$4 same impedance same matc\$4) and ((offset or compensat\$3 or cance\$4) same switch same (capacit\$4 or influen\$4 or reflec\$5))	US-PGPUB; USPAT; USOCR; DERWENT; IBM_TDB	OR	OFF	2011/07/07 14:41
L10	352	(induct\$4 same impedance same matc\$4) and ((offset or compensat\$3 or cance\$4) same switch same (capacit\$4 or influen\$4 or reflec\$5))	US-PGPUB; USPAT	OR	OFF	2011/07/07 14:42

L11	213	(induct\$4 same impedance same matc\$4) and ((offset or compensat\$3 or cance\$4) same switch same (capacit\$4 or influen\$4 or reflec\$5))	USPAT	OR	OFF	2011/07/07 14:42
L12	160	L11 and (@ad< "20020812" or @rlad< "20020812")	USPAT	OR	OFF	2011/07/07 14:43
L13	1067	(induct\$4 same impedance same matc\$4) and (switch same (capacit\$4 or influen\$4 or reflec\$5) same (remov\$3 or cance\$4 or compensat\$3))	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2011/07/07 14:57
L14	837	L13 and (@ad< "20020812" or @rlad< "20020812" or @pd< "20020812")	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2011/07/07 14:57
L15	8	L14 and (Ethernet or "802.3")	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2011/07/07 14:58
L16	494	(induct\$4 same impedance same matc\$4) and (switch same (capacit\$4 or influen\$4 or reflec\$5) same (remov\$3 or cance\$4 or compensat\$3) same (line or lines))	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2011/07/07 14:59
L17	428	L16 and (@ad< "20020812" or @rlad< "20020812" or @pd< "20020812")	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2011/07/07 14:59
L18	21	(impedance same matc\$4 same switch same ethernet)	US_PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2011/07/07 15:32
L19	4	muntz.in. and (reflection or reflections)	USPAT	OR	OFF	2011/07/07 15:38
L20	2	L19 and (impedance or impedance)	USPAT	OR	OFF	2011/07/07 15:40
L21	329	moore.xp. and impedance	USPAT	OR	OFF	2011/07/07 15:43
L22	254	moore.xp. and impedance and circuit	USPAT	OR	OFF	2011/07/07 15:43

L23	63	L22 and ohms	USPAT	OR	OFF	2011/07/07 15:43
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EAST Search History (Interference)

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7/7/11 4:21:38 PM

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